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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Tetsuya HIRAOKA, et al.**

Serial No.: **Not Yet Assigned**

Filed: **October 10, 2001**

For: **SEMICONDUCTOR DEVICE WITH STACK OF SEMICONDUCTOR CHIPS**

JCS86 U.S. PTO
09/973002
10/10/01

#2 / Priority
paper
1-7-02
R11010

CLAIM FOR PRIORITY UNDER 35 U.S.C. 119

Commissioner for Patents
Washington, D.C. 20231

October 10, 2001

Sir:

The benefit of the filing date of the following prior foreign application is hereby requested for the above-identified application, and the priority provided in 35 U.S.C. 119 is hereby claimed:

Japanese Appln. No. 2001-145238, filed May 15, 2001

In support of this claim, the requisite certified copy of said original foreign application is filed herewith.

It is requested that the file of this application be marked to indicate that the applicants have complied with the requirements of 35 U.S.C. 119 and that the Patent and Trademark Office kindly acknowledge receipt of said certified copy.

In the event that any fees are due in connection with this paper, please charge our Deposit Account No. 01-2340.

Respectfully submitted,
ARMSTRONG, WESTERMAN, HATTORI
McLELAND & NAUGHTON, LLP

William L. Brooks

William L. Brooks
Reg. No. 34,129

Atty. Docket No.: 011349
Suite 1000, 1725 K Street, N.W.
Washington, D.C. 20006
Tel: (202) 659-2930
Fax: (202) 887-0357
WLB/ll